

Interconnected Fabric Module

100

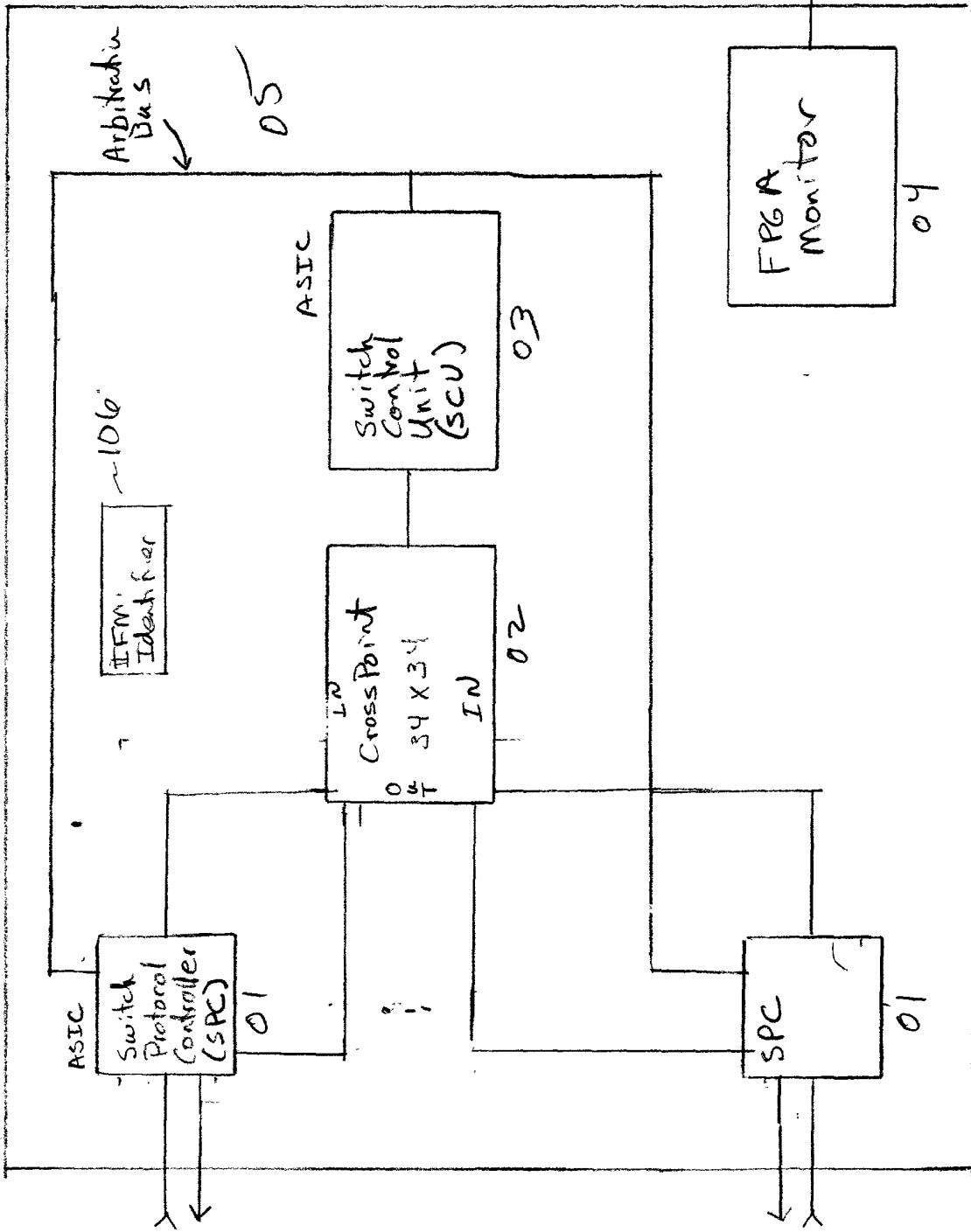


Fig 1

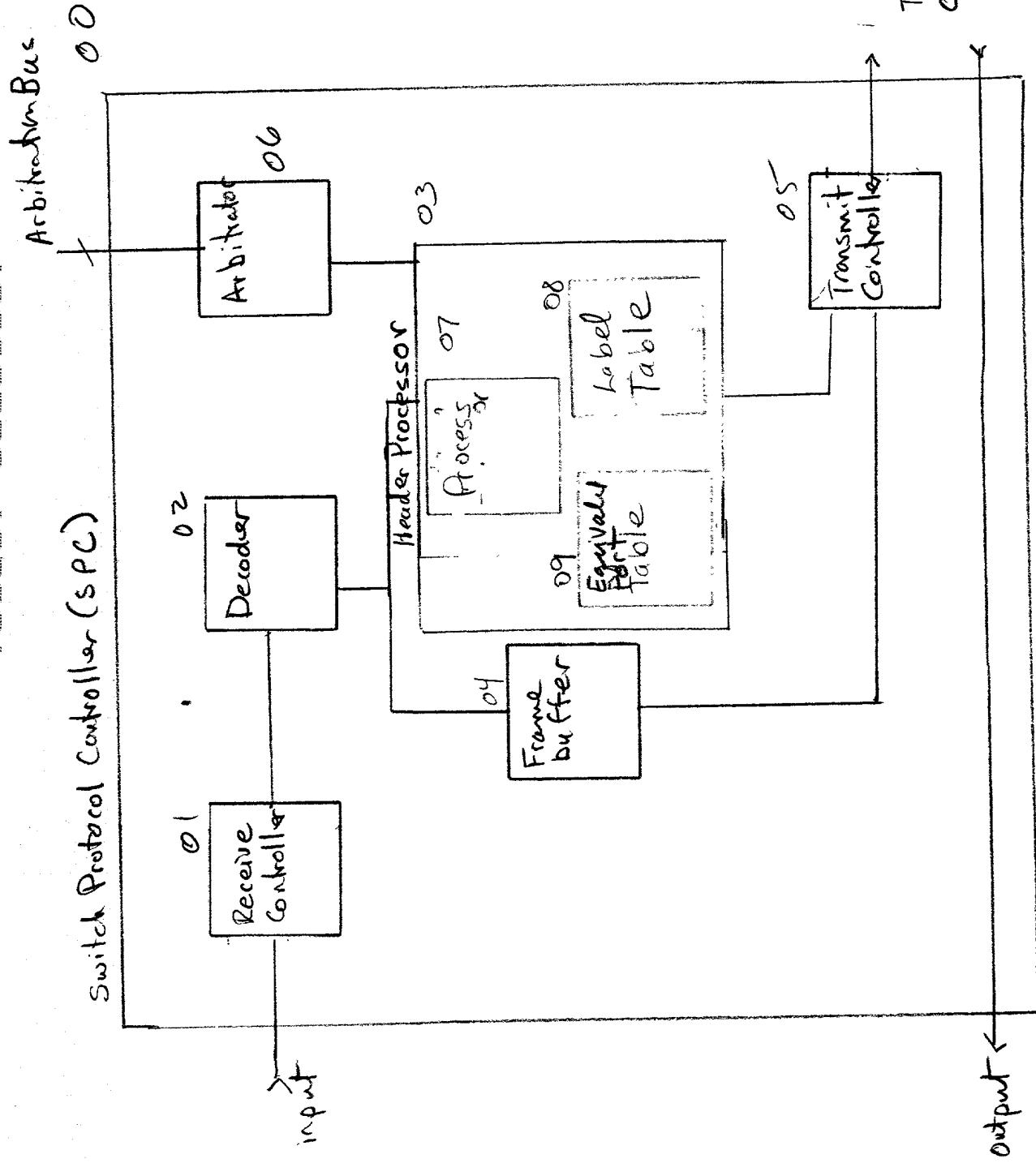


Fig 2

Label Table

Label Table	
Virtual Address	Port#
0	00
1	11
2	10
3	01
...	...
21	00
22	01
23	10
24	11
...	...
29	00
30	01
31	10
32	11

→ Port maps

Fig 3

Frame

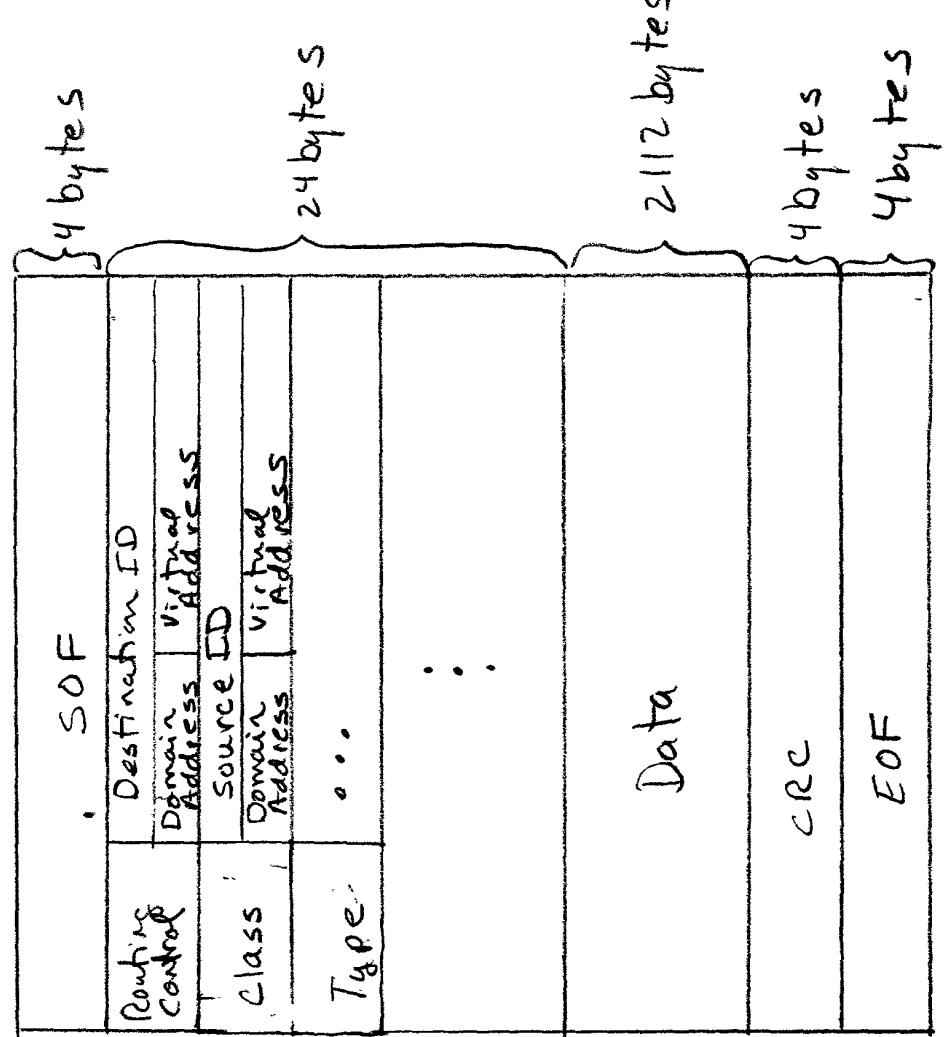


Fig 4

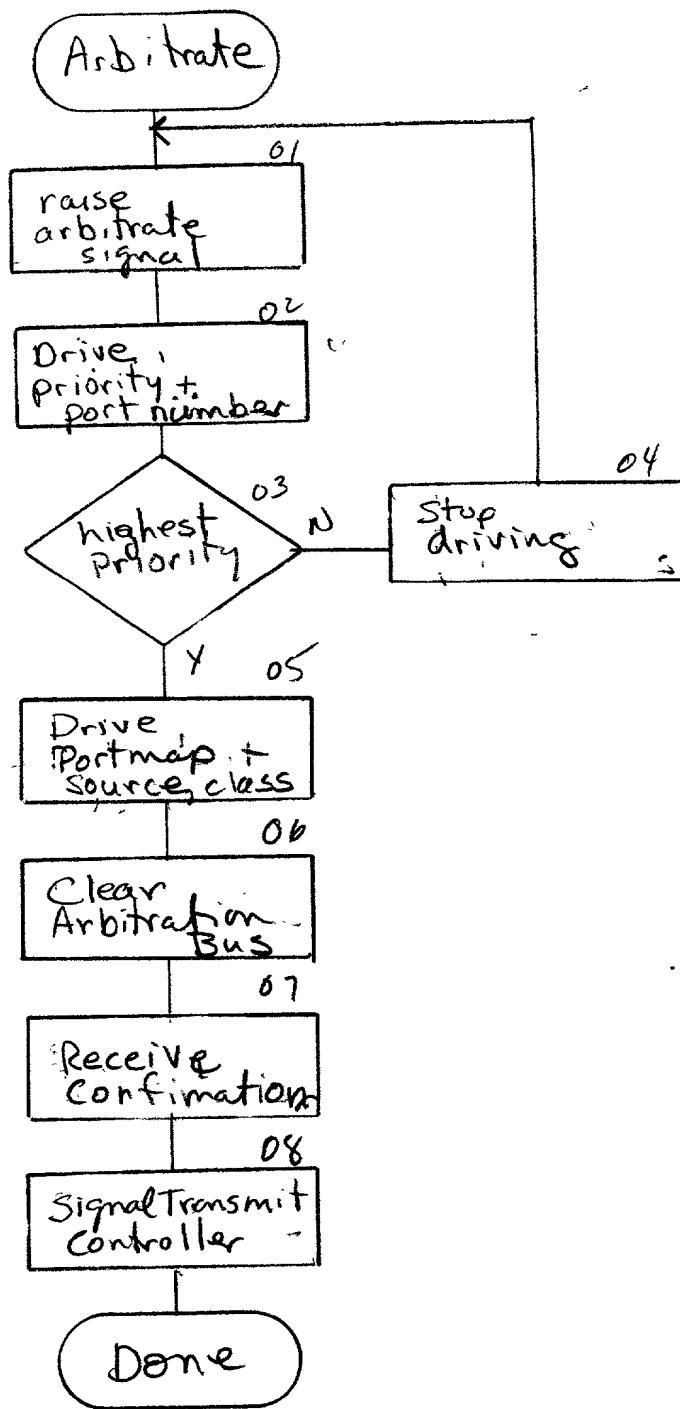


Fig 5

## Transmission Controller 00

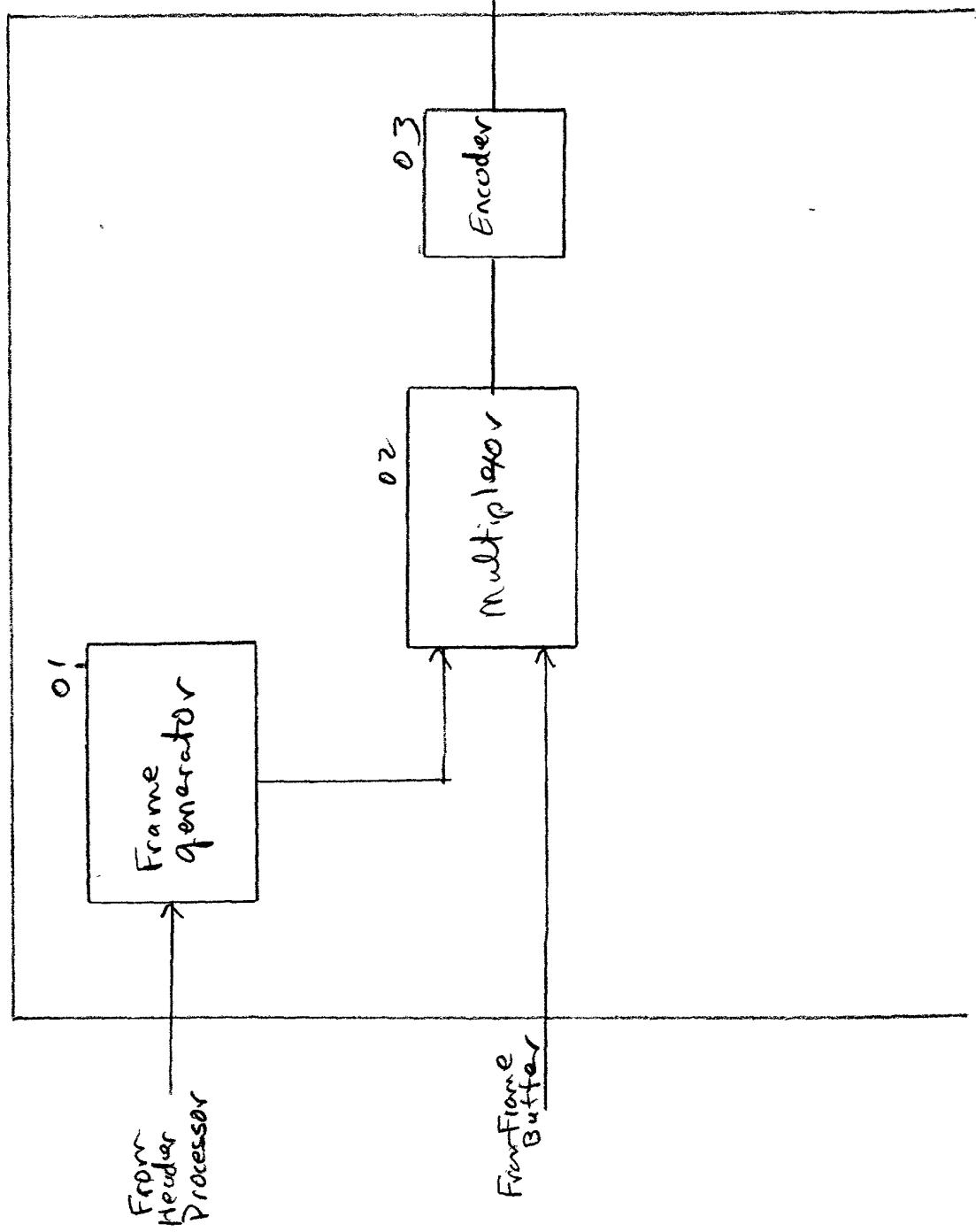
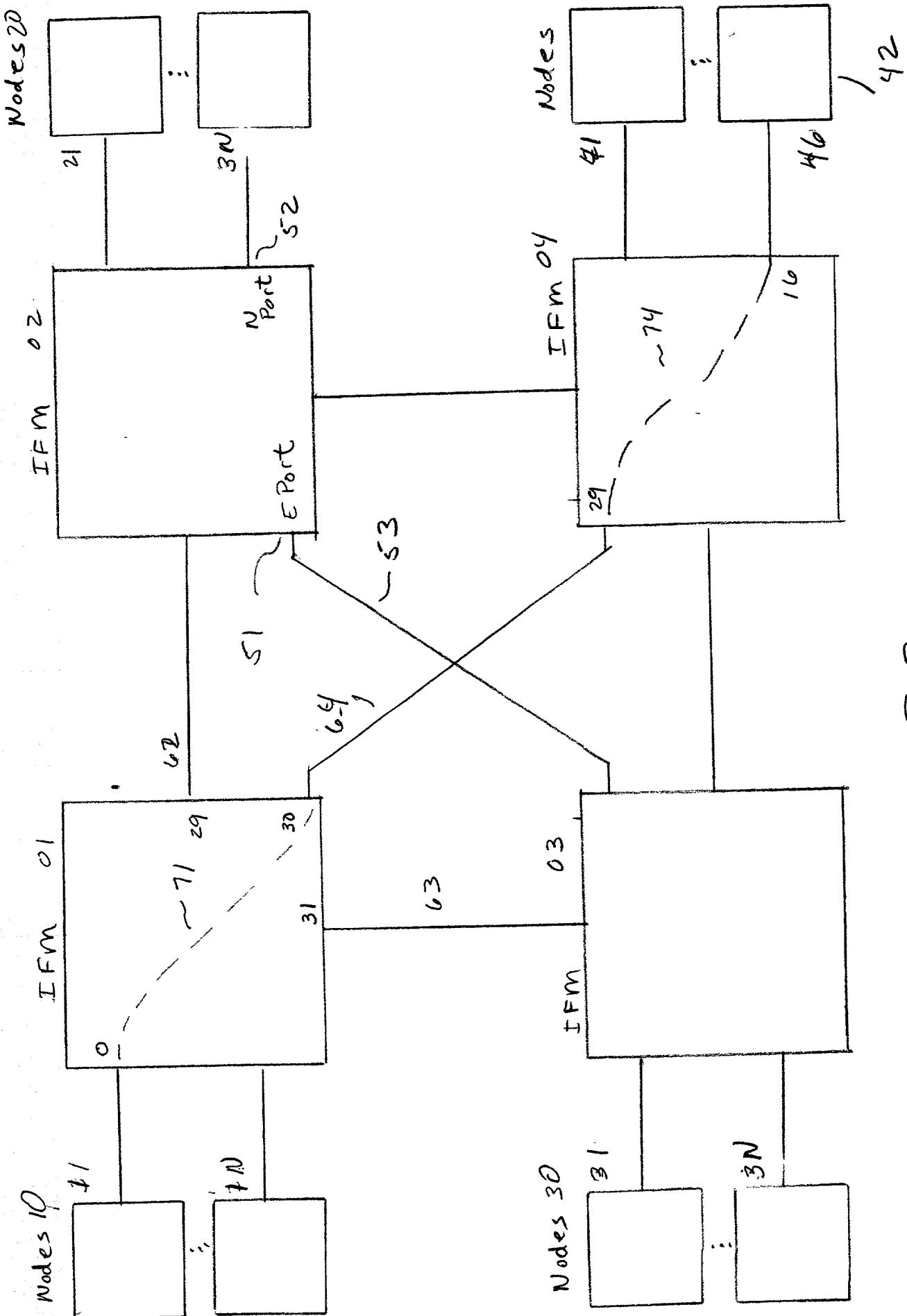


Fig 4



四  
五

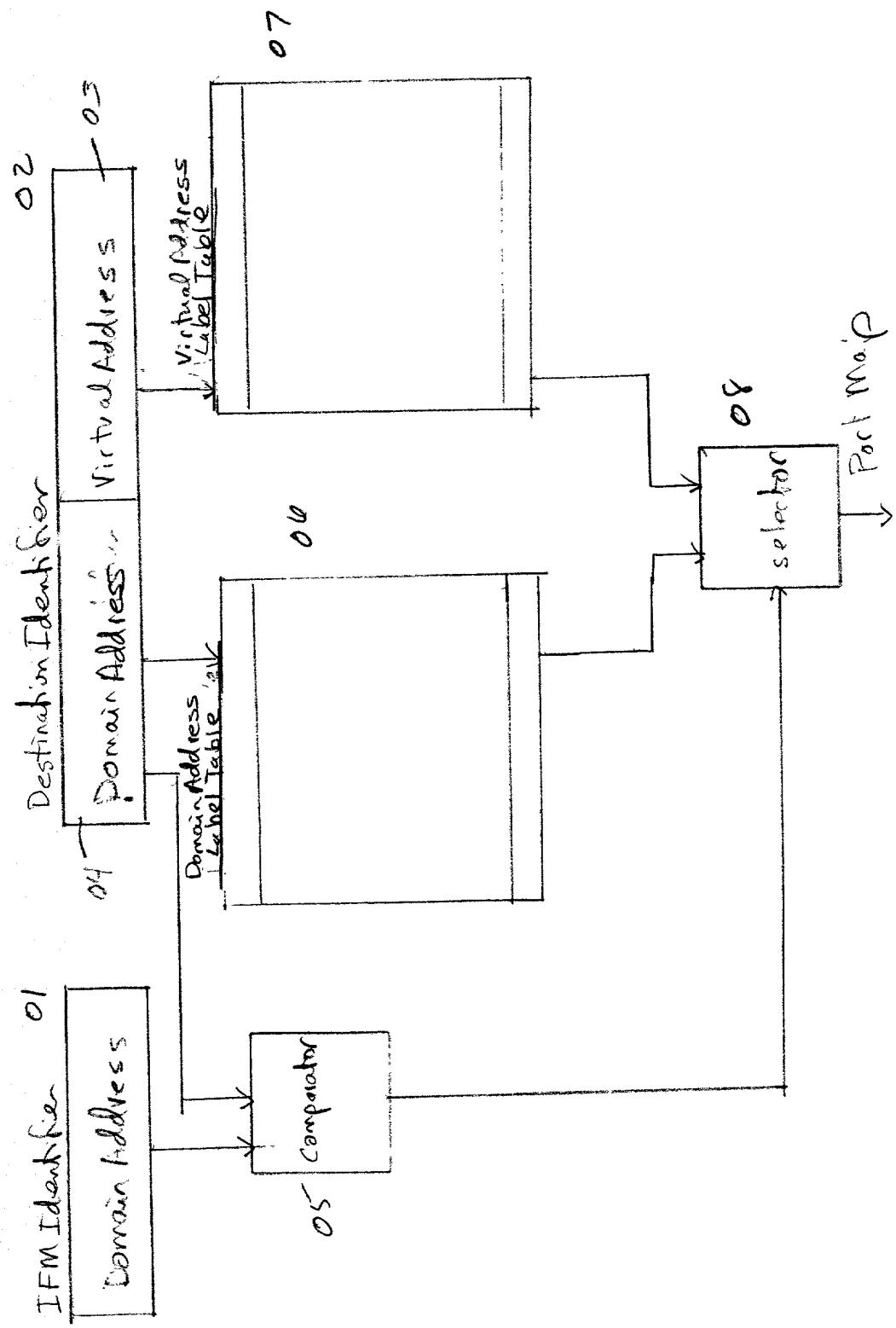


Fig. 8

## Quad Switch Protocol Controller

00

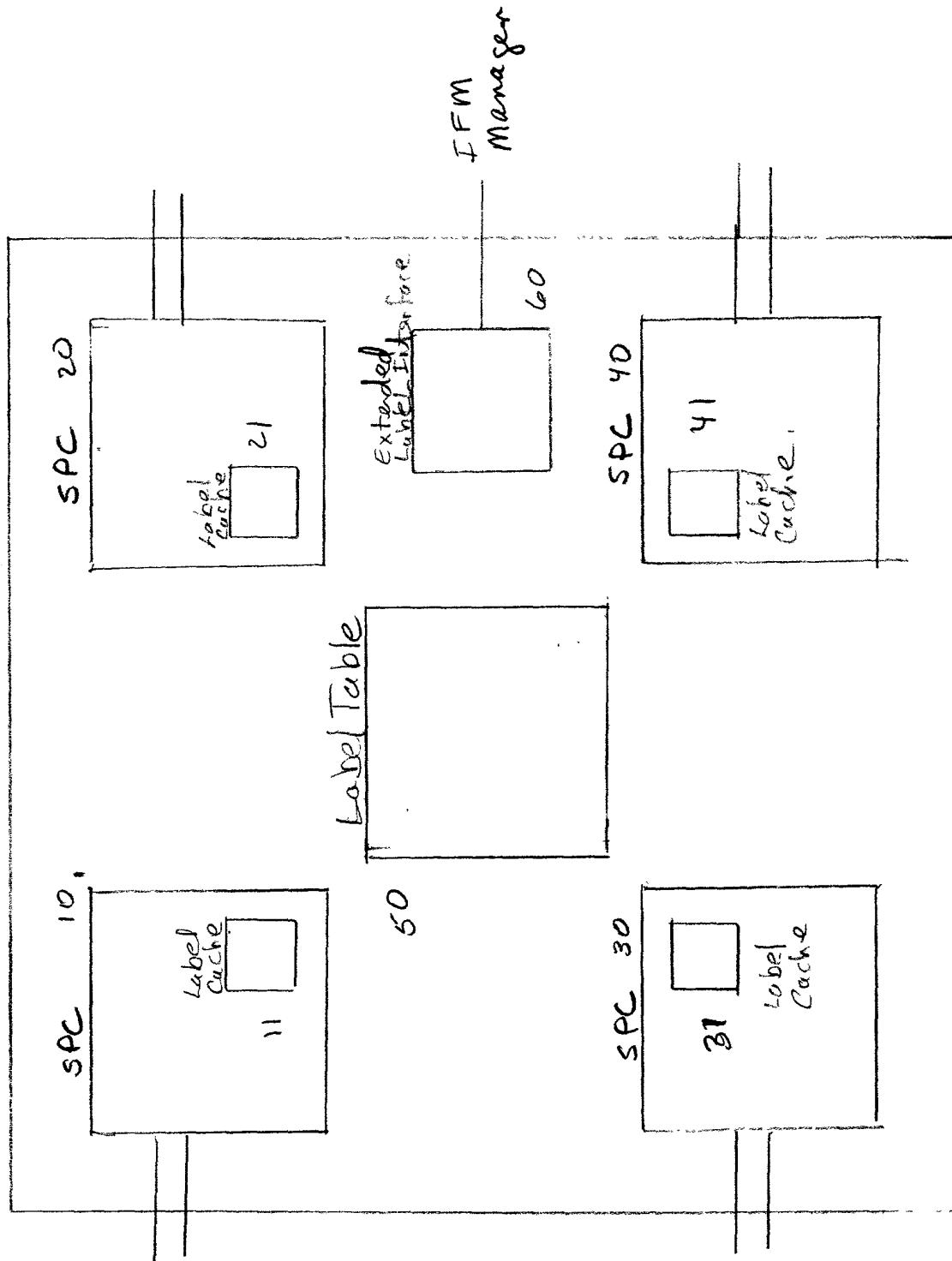


Fig. 9

00

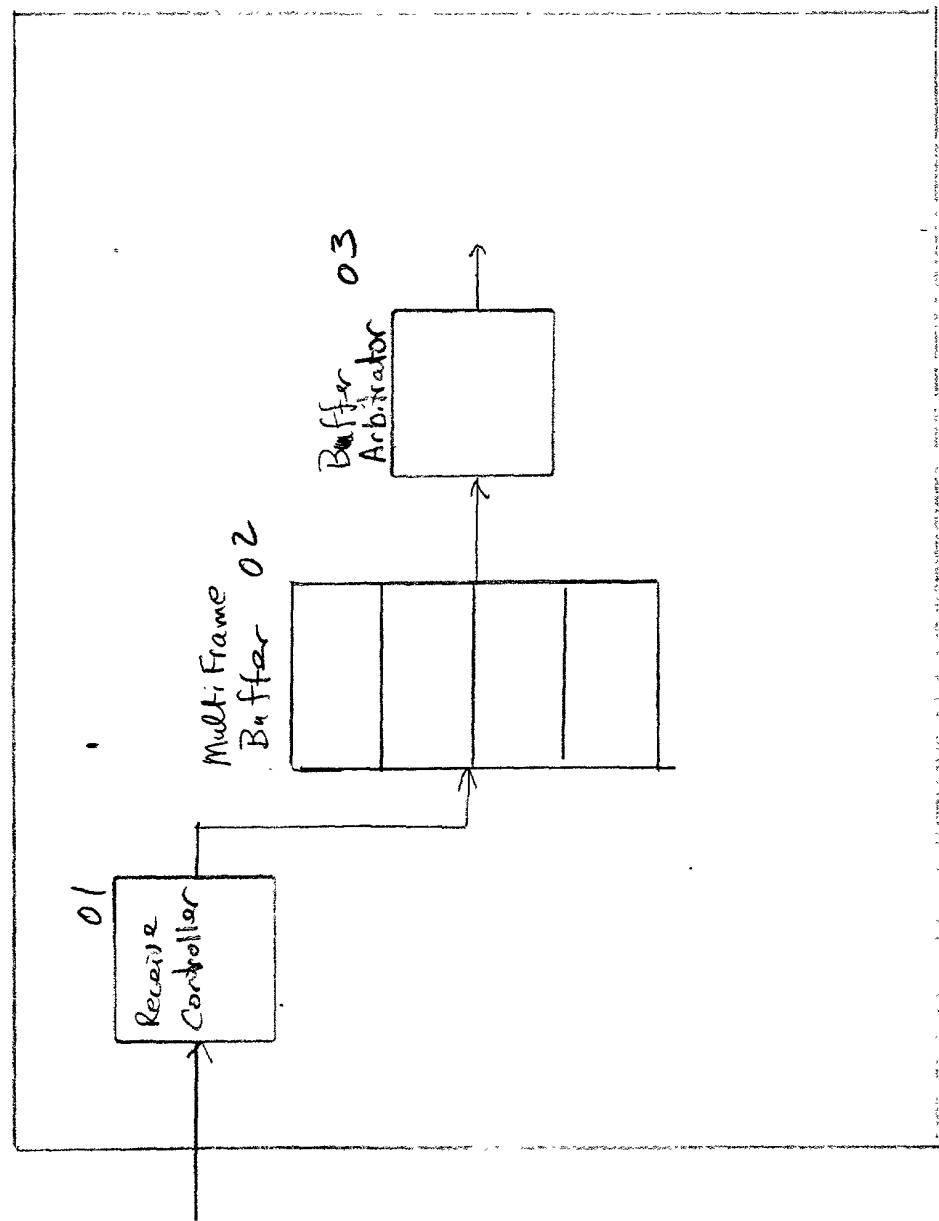


Fig 10

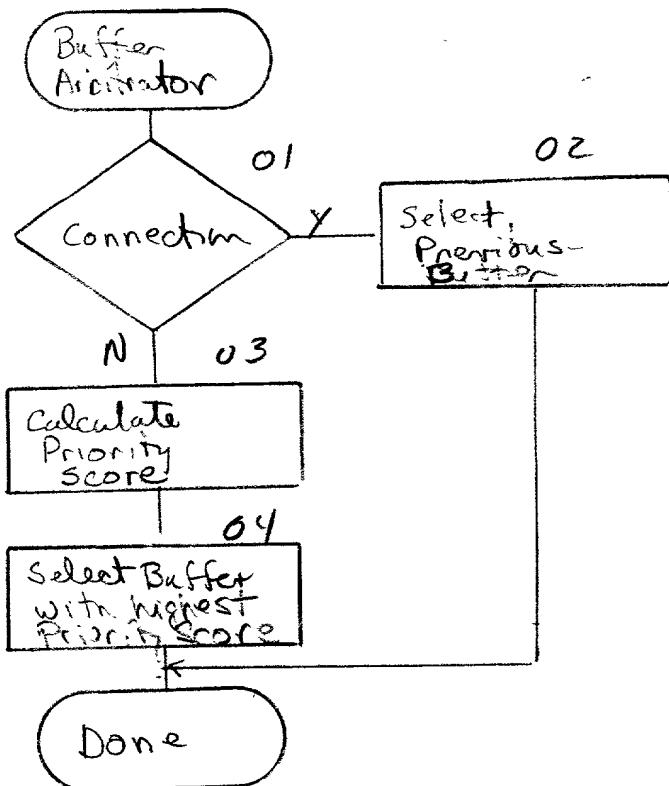


Fig 11

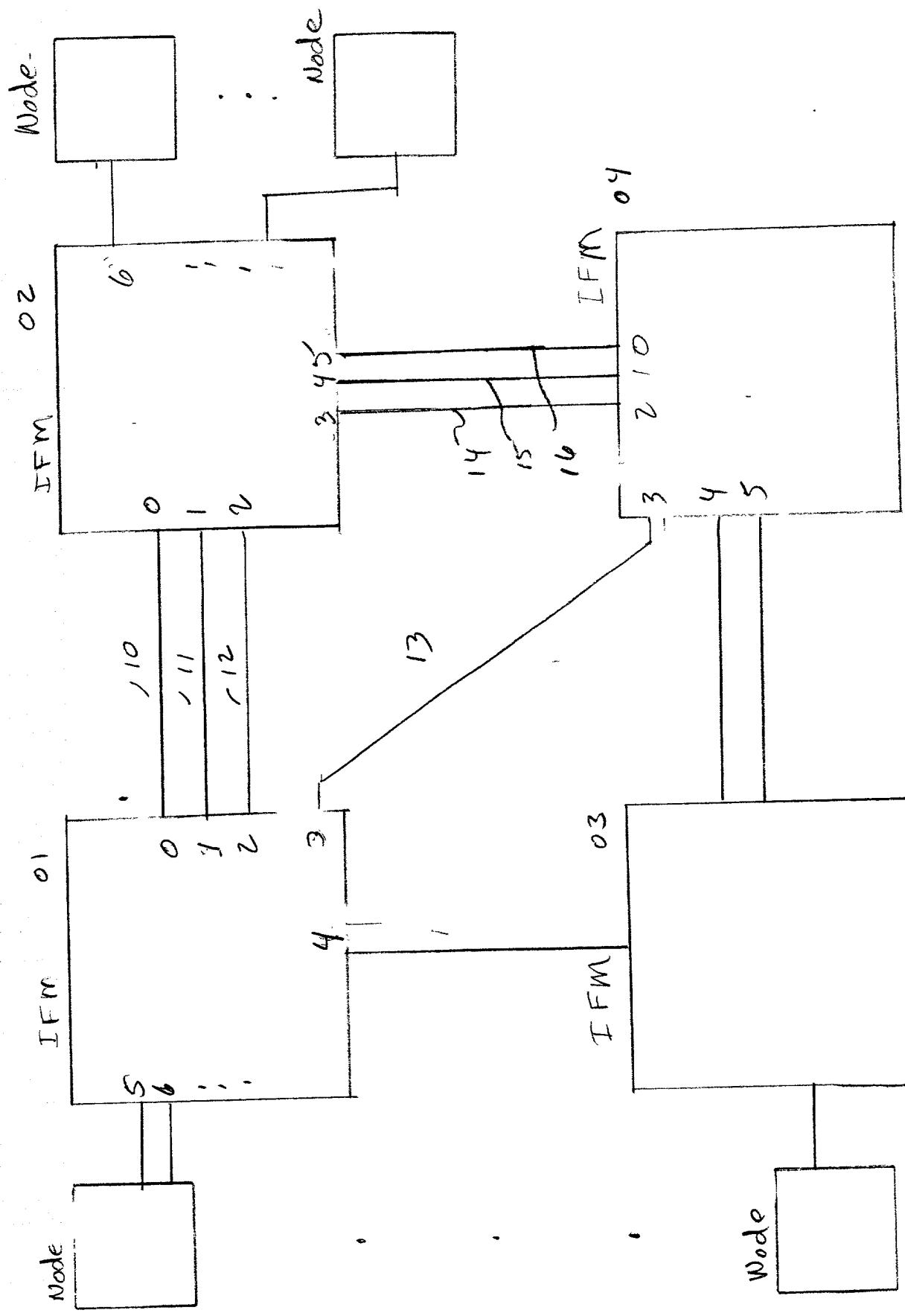


Fig 12

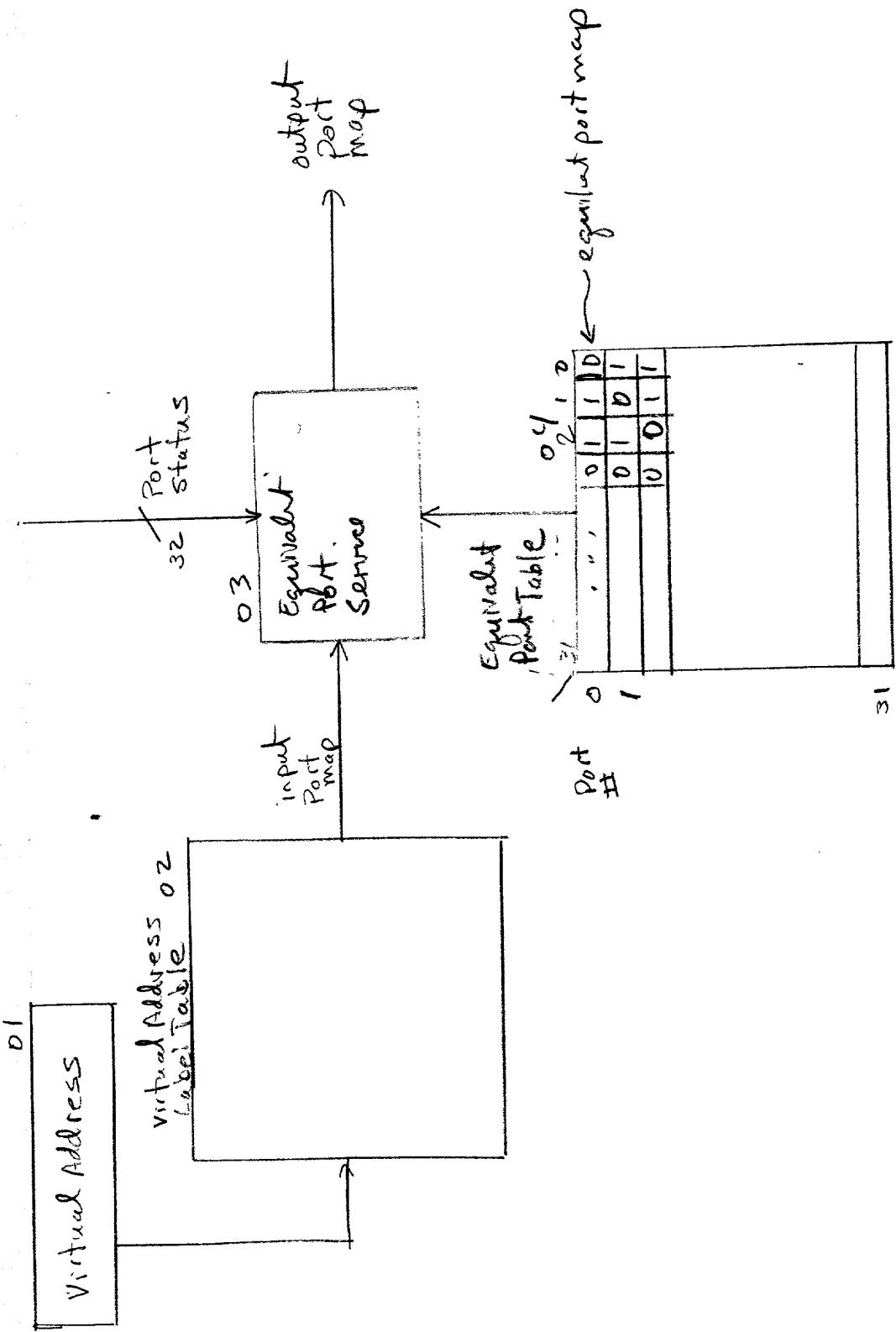


Fig 13

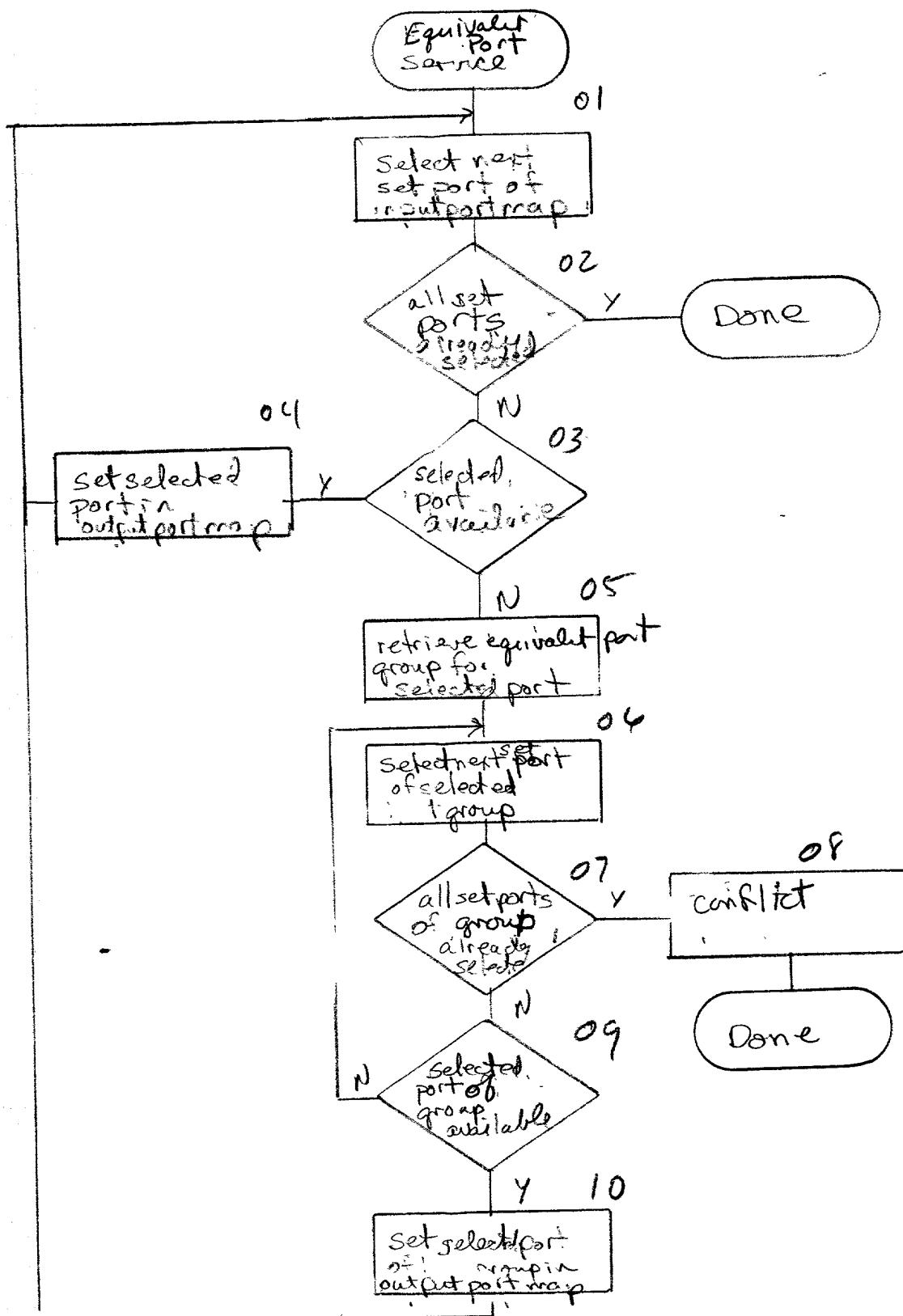


Fig 14

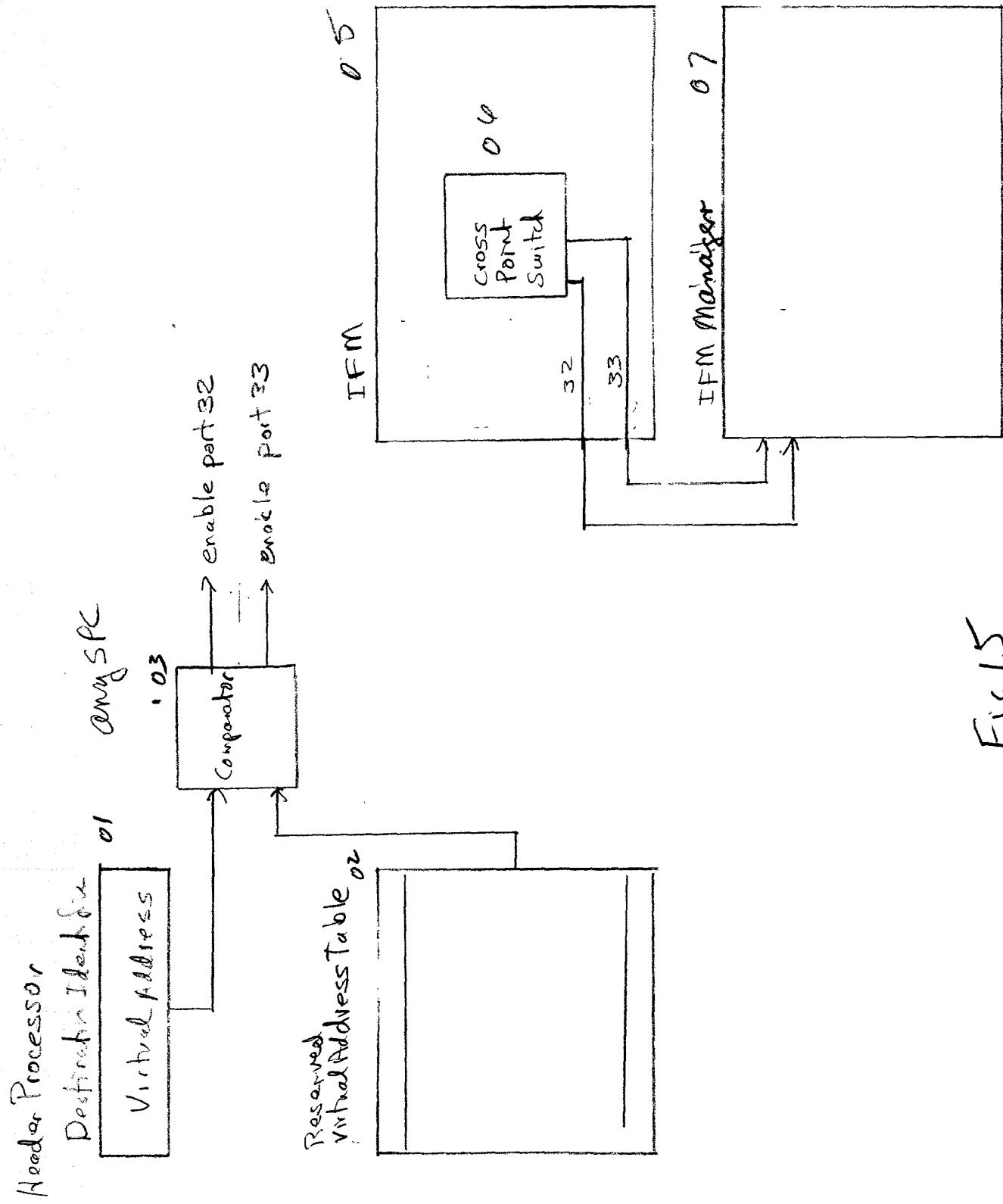
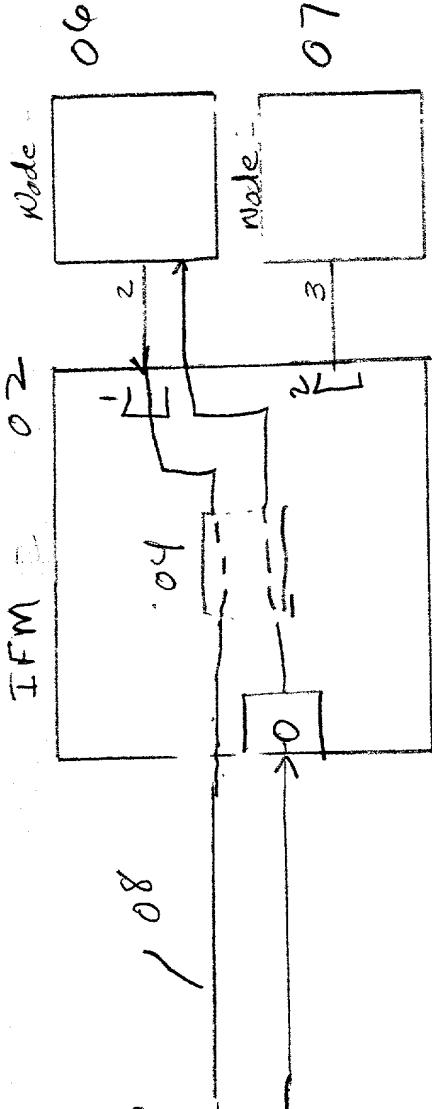
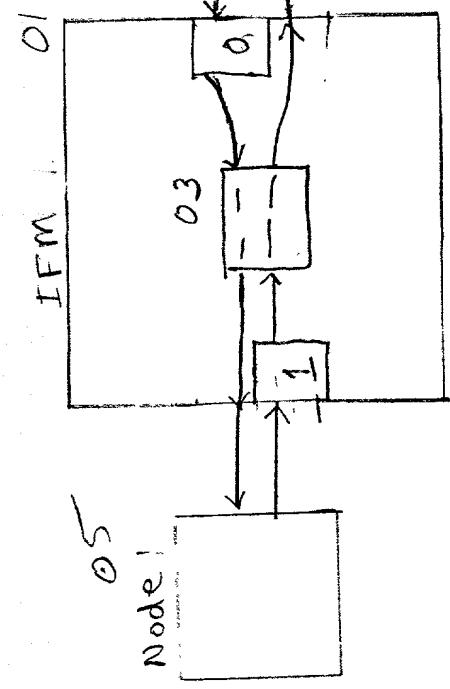


Fig 15



### Deadlock

1①

Time Node 05 T FM 1601 Node 06 T FM 1602  
0 send start connect

1	Connect $1 \leftrightarrow 0$	Forward start connect $2 \leftrightarrow 0$
2	Forward start connect $1 \leftrightarrow 0$	Forward start connect $2 \leftrightarrow 0$
3	Can't forward start connect Node 1	Can't forward start connect Node 2

Fig 16

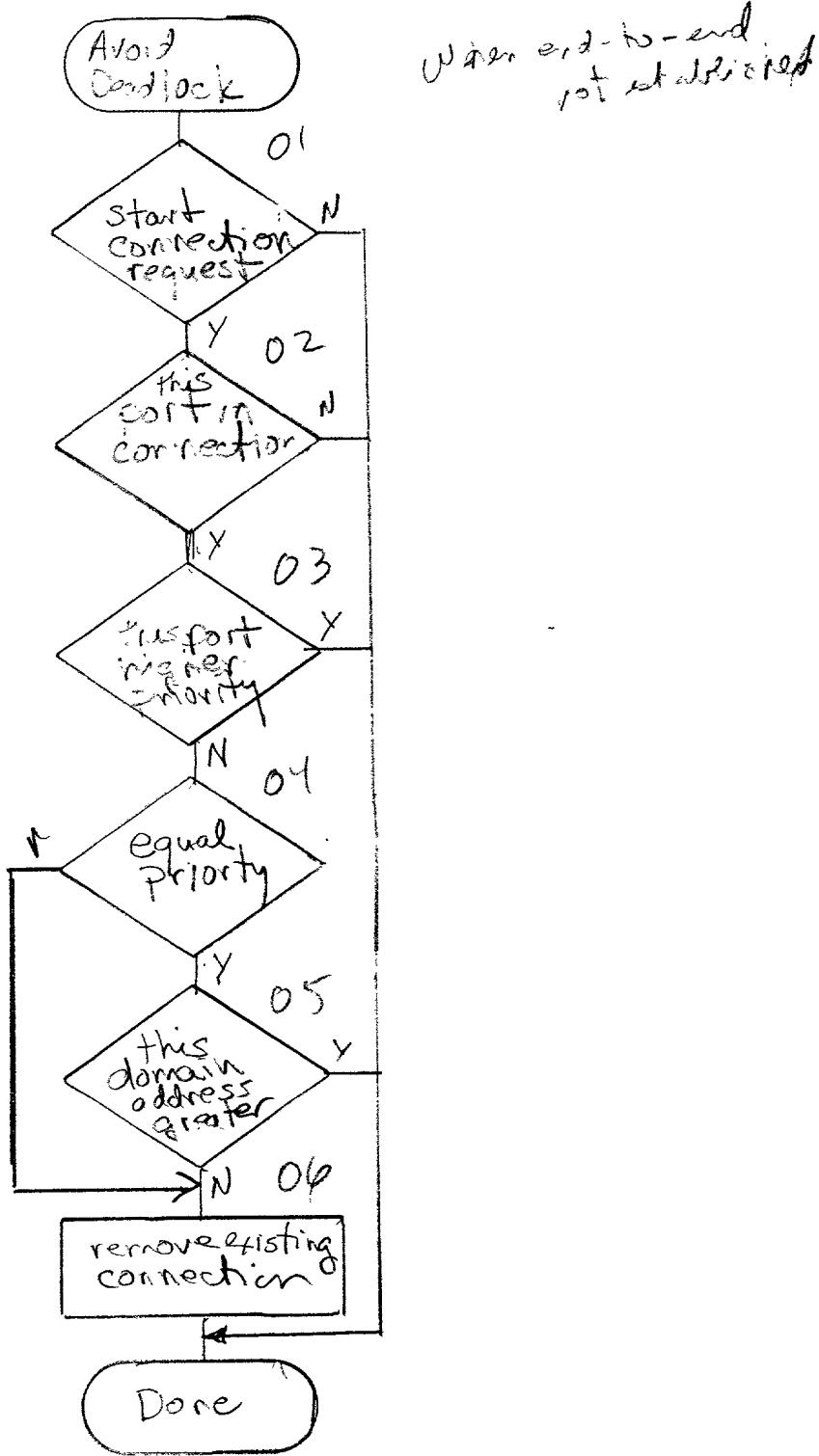


Fig 17

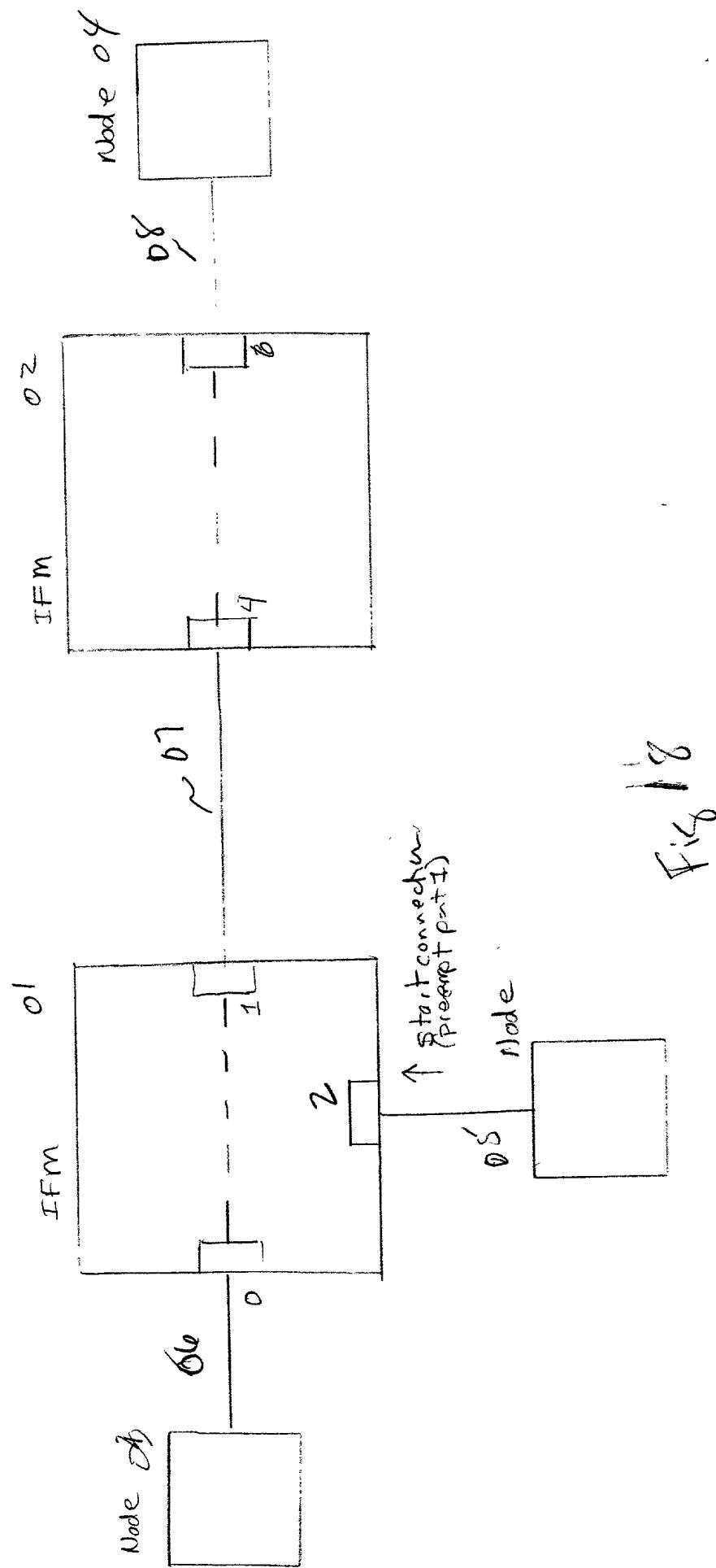
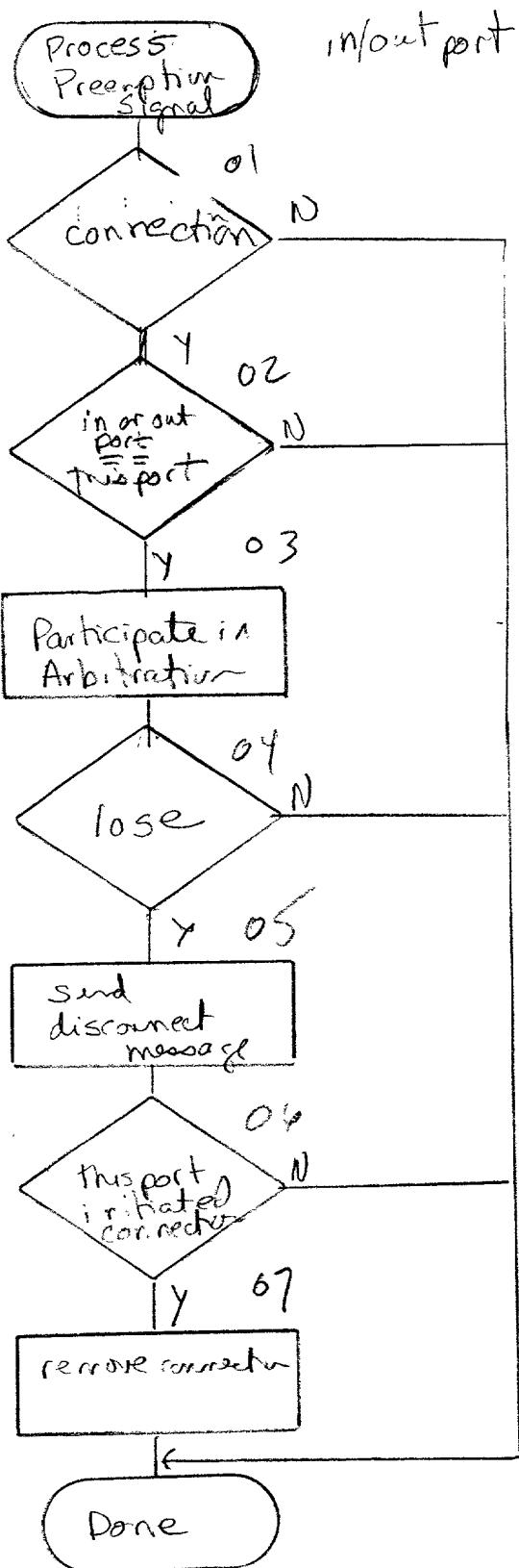


Fig 18



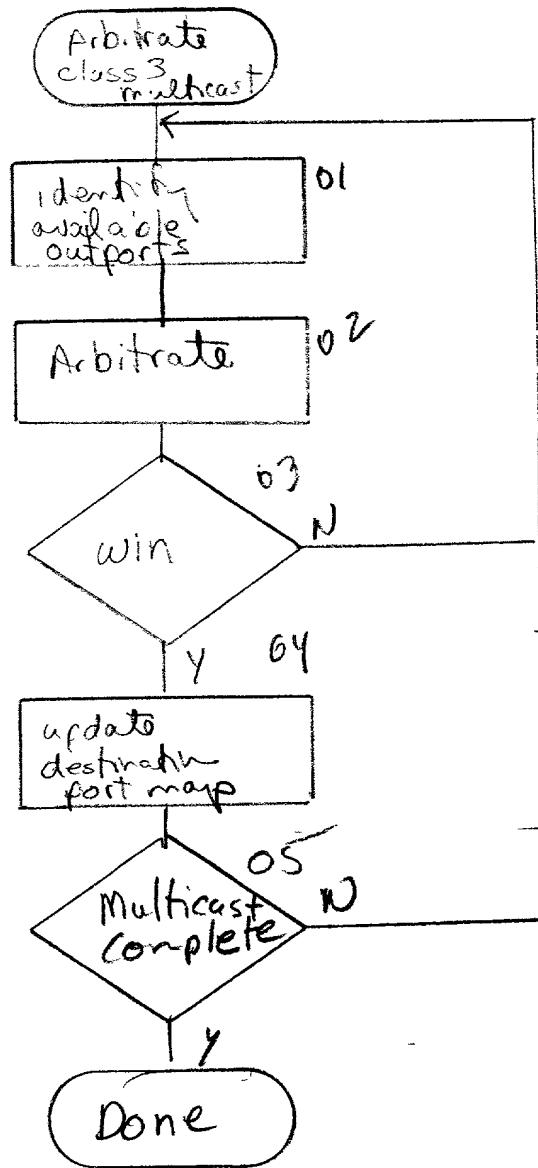


Figure 20